A SHARP BOUND ON THE SIZE OF A CONNECTED MATROID

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ABSTRACT. This paper proves that a connected matroid M whose largest circuit and largest cocircuit have c and c^* elements, respectively, has at most $\frac{1}{2}cc^*$ elements. It is also shown that if e is an element of M and c_e and c_e^* are the sizes of a largest circuit containing e and a largest cocircuit containing e, then $|E(M)| \leq (c_e - 1)(c_e^* - 1) + 1$. Both these bounds are sharp and the first is proved using the second. The second inequality is an interesting companion to Lehman's width-length inequality which asserts that the former inequality can be reversed for regular matroids when c_e and c_e^* are replaced by the sizes of a smallest circuit containing e and a smallest cocircuit containing e. Moreover, it follows from the second inequality that if u and v are distinct vertices in a 2-connected loopless graph G, then |E(G)| cannot exceed the product of the length of a longest (u, v)-path and the size of a largest minimal edge-cut separating u from v.

1. INTRODUCTION

At the 1991 Seattle conference on graph minors, Robin Thomas informally asked the question as to whether every sufficiently large connected matroid has a big circuit or a big cocircuit. This question was rapidly answered at that meeting by Lovász, Schrijver, and Seymour (see [9]) who proved the following result.

1.1. **Theorem.** Let M be a connected matroid with at least two elements. If a largest circuit of M has c elements and a largest cocircuit has c^* elements, then M has at most 2^{c+c^*-1} elements.

An affirmative answer to Thomas's question can also be obtained from a result of Tuza [15] for set systems. In response to Theorem 1.1, a natural question is whether the bound in the theorem can be improved, particularly if attention is restricted to certain special classes of matroids. For graphic matroids, the following dramatic improvement was found by Pou-Lin Wu [16].

1.2. **Theorem.** Let G be a loopless 2-connected graph in which a largest cycle has c edges and a largest bond has c^* edges. Then G has at most $\frac{1}{2}cc^*$ edges.

Of course, c is just the circumference of the graph G. There is a wide gap between the graph bound in Theorem 1.2 and the general matroid bound in Theorem 1.1. Reid [11] partially closed this gap. He defined the *matroid Ramsey number* n(s,t)to be the least positive integer n so that every n-element connected matroid has a circuit of size at least s or a cocircuit of size at least t. Reid showed that n(s,t)has several attractive properties that are analogous to properties of the classical Ramsey numbers for graphs established by Erdős and Szekeres [3]. Using these, Reid proved the following improvement of Theorem 1.1.

¹⁹⁹¹ Mathematics Subject Classification. 05B35.

1.3. Theorem. If M is a connected matroid with largest circuit of size c and largest cocircuit of size c^* , then

$$|E(M)| \le {\binom{c+c^*-2}{c-1}} - 2$$

provided both c and c^* exceed three.

Reid [11] and Hurst and Reid [5] also determined some small matroid Ramsey numbers including all n(s,t) with s or t at most three.

The purpose of this paper is to prove that the bound that Pou-Lin Wu proved for graphic matroids also holds for matroids in general. The following is our main result.

1.4. Theorem. Let M be a connected matroid with at least two elements. If a largest circuit of M has c elements and a largest cocircuit has c^* elements, then

$$|E(M)| \le \left\lfloor \frac{cc^*}{2} \right\rfloor.$$

This theorem solves a problem raised by Oxley [10] and settles a conjecture of Bonin, McNulty, and Reid [1]. For comparison, a lower bound on |E(M)| in terms of c and c^* that holds for all matroids M having non-zero rank and corank is

$$c + c^* - 2 \le |E(M)|.$$
 (1)

To see this, observe that $c \leq r(M) + 1$ and $c^* \leq r^*(M) + 1$. The bound in (1) is sharp. It is attained by, for example, the whirls, all uniform matroids of non-zero rank and corank, and, more generally, by all matroids with both a free element and a cofree element.

The proof of Theorem 1.4 will use another bound that is of independent interest.

1.5. Theorem. Let M be a connected matroid with at least two elements and let e be an element of M. If a largest circuit of M containing e has c_e elements and a largest cocircuit containing e has c_e^* elements, then

$$|E(M)| \le (c_e - 1)(c_e^* - 1) + 1.$$

The last theorem has an attractive similarity to a bound derived from the widthlength inequality [7, 4]. In a matroid M, let e be an element that is neither a loop nor a coloop. Let $\lambda(M) + 1$ and $\omega(M) + 1$ be the cardinalities of a smallest circuit containing e and a smallest cocircuit containing e. Lehman [7] showed that if M is regular, then

$$\lambda(M)\omega(M) \le |E(M)| - 1. \tag{2}$$

More generally, it follows from a result of Seymour [12] that the last inequality holds for all binary matroids that have no F_7^* -minor using *e*. In fact, it also holds for F_7^* , although it fails, for example, for AG(3, 2).

A straightforward consequence of Theorem 1.5 is the following result for graphs.

1.6. Corollary. Let u and v be distinct vertices in a 2-connected loopless graph G. Then |E(G)| cannot exceed the product of the length of a longest (u, v)-path and the size of a largest minimal edge-cut separating u from v.

The bounds in Theorems 1.2, 1.4, and 1.5 are sharp. Pou-Lin Wu [17] determined all graphs that attain the bound in Theorem 1.2. The cycle matroid of each such graph attains the bound in Theorem 1.4 provided c or c^* is even. A non-graphic matroid attaining the bound in Theorem 1.4, which was noted by Reid [11], is AG(3,2). In Section 4, we shall describe all the matroids that attain the bound in Theorem 1.5, noting that there is a close link between these matroids and those found by Wu.

The matroid terminology used here will follow Oxley [9]. In particular, the set of circuits of a matroid M will be denoted by $\mathcal{C}(M)$. Theorem 1.5 will be deduced in the next section from a stronger result, while Theorem 1.4 will be proved in Section 3.

2. Two strengthenings of Theorem 1.5

Let e be an element of a matroid M and suppose that e is not a coloop of M. We define the *circumference of* M through e by

$$c_e(M) = \max\{|C| : e \in C \in \mathcal{C}(M)\},\$$

that is, $c_e(M)$ is the size of a largest circuit that contains e. If f is an element of M that is not a loop, we define $c_f^*(M) = c_f(M^*)$. The *circumference* c(M) of a non-free matroid is the size of a largest circuit of M. If r(M) > 0, we define $c^*(M) = c(M^*)$.

Let C be a circuit of a matroid M. A subset P of E(M) - C is a chord of C if $P \in \mathcal{C}(M/C) - \mathcal{C}(M)$. Equivalently, P is a chord of C if and only if P is disjoint from C and $M|(C \cup P)$ is a connected matroid having corank two. In particular, when P is a chord of C, it is a series class of $M|(C \cup P)$.

The first strengthening of Theorem 1.5 will require some more preliminaries. Let L be a subset of the ground set of a matroid M and suppose that L is the union of a set of circuits of M and $r^*(M|L) = 2$. Then L is what Tutte [14] has called a "line" of M. We shall call L a *Tutte-line* since the word "line" is also commonly used in matroid theory to mean a rank-2 flat. It is not difficult to see that every Tutte-line L of a matroid M has a canonical partition $\{L_1, L_2, \ldots, L_k\}$ such that a subset C of L is a circuit of M|L if and only if $C = L - L_i$ for some i in $\{1, 2, \ldots, k\}$. A Tutte-line L is connected if M|L is a connected matroid. The size of a Tutte-line is the number of sets in its canonical partition. Hence a Tutte-line is connected if and only if it has size at least three.

2.1. **Theorem.** Let M be a connected matroid such that |E(M)| > 1 and let e be an element of M. Then

$$|E(M)| \le c_e(M) + \frac{1}{m-2}(c_e(M) - 1)(c_e^*(M) - 2)$$

for all integers m exceeding two such that every connected Tutte-line of M has size at least m.

The dual of this theorem can be restated using the concept of a *coline* of a matroid, a flat of rank two less than the matroid.

2.2. Corollary. Let M be a connected matroid such that |E(M)| > 1 and let e be an element of M. Suppose that m is an integer exceeding two such that every coline of M that is contained in more than two hyperplanes is contained in at least m hyperplanes. Then

$$|E(M)| \le c_e^*(M) + \frac{1}{m-2}(c_e^*(M) - 1)(c_e(M) - 2).$$

Proof of Theorem 1.5. In all connected matroids with at least two elements, every connected Tutte-line has size at least three. The theorem follows immediately by taking m = 3 in Theorem 2.1.

Proof of Corollary 1.6. Add the edge e to G so that it joins u and v. Then the result follows immediately from Theorem 1.5.

Proof of Theorem 2.1. We argue by induction on |E(M)| noting that the result holds when |E(M)| = 2. Assume it holds for |E(M)| < k and let $|E(M)| = k \ge 3$. Choose a circuit C of M such that $e \in C$ and $|C| = c_e(M)$. Observe that the result holds when E(M) = C. Thus we may suppose that $E(M) \neq C$. Let M_1, M_2, \ldots, M_n be the connected components of M/C.

For each element f of C - e, consider the connected component N_f of $M/(C - \{e, f\})$ that contains f. There is a set \mathcal{P}_f of matroids such that

- (i) N_f is the parallel connection of the matroids in \mathcal{P}_f across the basepoint e; and
- (ii) N/e is connected for every N in \mathcal{P}_f .

Observe that $\{e, f\}$ is the ground set of a matroid that belongs to \mathcal{P}_f . For each matroid N in \mathcal{P}_f , let C_N^* be a cocircuit of N such that $e \in C_N^*$ and $|C_N^*| = c_e^*(N)$. Let

$$C^* = \bigcup_{N \in \mathcal{P}_f} C_N^*.$$

Then C^* contains e and is a cocircuit of N_f and hence of M. Thus

$$c_e^*(M) \ge |C^*| = 1 + \sum_{N \in \mathcal{P}_f} (c_e^*(N) - 1).$$
 (3)

For each f in C - e, view the set \mathcal{P}_f as a multiset and let \mathcal{P} be the multiset that is the union of all these multisets. For each i in $\{1, 2, \ldots, n\}$, let \mathcal{Q}_i be the multiset of matroids N in \mathcal{P} for which $N/e = M_i$. Then \mathcal{P} is the union of the multisets $\mathcal{Q}_1, \mathcal{Q}_2, \ldots, \mathcal{Q}_n$ together with |C| - 1 copies of $U_{1,2}$, the ground sets of the latter being all sets of the form $\{e, f\}$ such that $f \in C - e$. For all i in $\{1, 2, \ldots, n\}$, let N_i be a matroid in \mathcal{Q}_i for which

$$c_e^*(N_i) = \min\{c_e^*(N) : N \in \mathcal{Q}_i\}.$$
(4)

On summing (3) over all f in C - e, we get

$$(|C|-1)c_e^*(M) \ge (|C|-1) + \sum_{f \in C-e} \sum_{N \in \mathcal{P}_f} (c_e^*(N)-1).$$

Therefore

$$(|C|-1)c_e^*(M) \ge (|C|-1) + \left[(|C|-1) + \sum_{i=1}^n \sum_{N \in \mathcal{Q}_i} (c_e^*(N)-1) \right], \quad (5)$$

where the extra (|C| - 1)-term appears because, for every f in C - e, there is a matroid belonging to \mathcal{P}_f having ground set $\{e, f\}$ and this matroid does not belong to any \mathcal{Q}_i . Hence

$$(|C|-1)(c_e^*(M)-2) \ge \sum_{i=1}^n |\mathcal{Q}_i|(c_e^*(N_i)-1).$$
(6)

As $|C| = c_e(M)$, it follows that

$$(c_e(M) - 1)(c_e^*(M) - 2) \ge \sum_{i=1}^n |\mathcal{Q}_i|(c_e^*(N_i) - 1).$$
(7)

Next we obtain, for each i in $\{1, 2, ..., n\}$, a lower bound on $|Q_i|$. Let P_i be a subset of $E(M_i)$ such that $P_i \cup e$ is a circuit of N_i of size $c_e(N_i)$. Thus

$$|P_i| = c_e(N_i) - 1. (8)$$

Moreover, P_i is a chord of C, so $C \cup P_i$ is a connected Tutte-line L_i of M. Let $\{P_i, L_{i1}, L_{i2}, \ldots, L_{in_i}\}$ be the canonical partition of L_i . Without loss of generality, we may assume that $e \in L_{in_i}$. For all j in $\{1, 2, \ldots, n_i - 1\}$, the set $L_i - L_{ij}$ is a circuit of M containing e. Thus

$$|C| \ge |L_i - L_{ij}| = |L_i| - |L_{ij}|.$$

 $|C| = |L_i - P_i| = |L_i| - |P_i|.$

Hence

But

$$|P_i| \le \min\{|L_{ij}| : 1 \le j \le n_i - 1\}.$$
(9)

For all f in $\bigcup_{j=1}^{n_i-1} L_{ij}$, since M_i and N_f are the connected components of M/Cand $M/(C - \{e, f\})$, respectively, containing P_i , we deduce that $E(M_i) \subseteq E(N_f)$ and therefore \mathcal{P}_f contains a member N of \mathcal{Q}_i . Thus

$$|\mathcal{Q}_i| \ge \sum_{j=1}^{n_i - 1} |L_{ij}| \ge (n_i - 1)|P_i|, \tag{10}$$

where the second inequality follows by (9). Moreover, since $C \cup P_i$ is a connected Tutte-line of M of size $n_i + 1$, we have $n_i + 1 \ge m$. Hence, using (10) and (8), we deduce that

$$|\mathcal{Q}_i| \ge (m-2)|P_i| = (m-2)(c_e(N_i) - 1).$$
(11)

On combining (7) and (11), we get

$$(c_e(M) - 1)(c_e^*(M) - 2) \ge \sum_{i=1}^n (m - 2)(c_e(N_i) - 1)(c_e^*(N_i) - 1).$$
(12)

As $2 \leq |E(N_i)| < |E(M)|$, the theorem holds for N_i . In particular, since every connected Tutte-line of N_i has size at least three, it follows that

$$|E(N_i)| \le c_e(N_i) + (c_e(N_i) - 1)(c_e^*(N_i) - 2).$$

But $|E(M_i)| = |E(N_i)| - 1$, so

$$|E(M_i)| \le (c_e(N_i) - 1)(c_e^*(N_i) - 1).$$
(13)

On combining (12) and (13), we get

$$(c_e(M) - 1)(c_e^*(M) - 2) \geq \sum_{i=1}^n (m-2)|E(M_i)|$$

$$\geq (m-2)\sum_{i=1}^n |E(M_i)| = (m-2)|E(M) - C|.$$

Rewriting the last inequality, using the fact that $c_e(M) = |C|$, we obtain

$$|E(M)| \le c_e(M) + \frac{1}{m-2}(c_e(M) - 1)(c_e^*(M) - 2)$$

and the theorem follows by induction.

As an immediate consequence of Corollary 2.2, we have the following result.

2.3. Corollary. Let M be a connected matroid such that |E(M)| > 1. Suppose that m is an integer exceeding two such that every coline of M that is contained in more than two hyperplanes is contained in at least m hyperplanes. Then

$$|E(M)| \le c^*(M) + \frac{1}{m-2}(c^*(M) - 1)(c(M) - 2).$$

The bound in the last inequality is sharp, it being attained for every $m \ge 3$ by $U_{n-m+2,n}$ provided $n \ge m-1$.

After the original submission of this paper, Guoli Ding (private communication) gave a short proof of Theorem 1.5. A straightforward modification of his argument yields our second strengthening of Theorem 1.5. This result answers a question of Paul Seymour (private communication).

2.4. Theorem. Let M be a connected matroid with at least two elements and let e be an element of M. Then there are $c_e(M) - 1$ cocircuits of M each containing e such that the union of these cocircuits is E(M).

Proof. We argue by induction on $c_e(M)$. If $c_e(M) = 2$, then M is a uniform matroid having rank one, so E(M) is a cocircuit of M and the result follows. Now suppose that the theorem holds for $c_e(M) < n$ and let $c_e(M) = n \ge 3$.

Let C^* be a cocircuit of M that contains e. Clearly $C^* \neq E(M)$. By a wellknown result of Tutte [14] (see also [9, Theorem 4.3.1]), for every element z of a connected matroid, the deletion or contraction of z leaves a connected matroid. Thus there is a partition $\{X, Y\}$ of $C^* - e$ such that $M \setminus X/Y$ is a connected matroid; call it N. As every circuit of M that contains e must also meet X or Y, it follows that

$$c_e(N) < c_e(M).$$

Since $C^* \neq E(M)$, the matroid N has at least two elements. Thus, by the induction assumption, for some $k \leq c_e(N) - 1$, there are k cocircuits $C_1^*, C_2^*, \ldots, C_k^*$ of N each containing e such that the union of these cocircuits is E(N). For each C_i^* , there is a cocircuit D_i^* of M such that $C_i^* = D_i^* - X$. Hence $C^*, D_1^*, D_2^*, \ldots, D_k^*$ are cocircuits of M each containing e and

$$E(M) = E(N) \cup X \cup Y \subseteq C_1^* \cup C_2^* \cup \dots \cup C_k^* \cup C^* \subseteq D_1^* \cup D_2^* \cup \dots \cup D_k^* \cup C^*.$$

Thus we have a family of k + 1 cocircuits of M that covers E(M). Since

$$k+1 \le (c_e(N)-1)+1 = c_e(N) \le c_e(M)-1,$$

the result follows by induction.

We observe, from the last proof, that the family of $c_e(M) - 1$ cocircuits that covers E(M) can be required to include any arbitrarily chosen cocircuit of M that contains e.

3. Proof of Theorem 1.4

In this section, we shall prove Theorem 1.4. In addition to Theorem 1.5, the proof will use two lemmas and we begin the section by proving them. For a circuit C of a matroid M such that $|C| \ge 2$, we define

$$c^*(C, M) = \max\{|D^*| : D^* \in \mathcal{C}(M^*) \text{ and } |D^* \cap C| = 2\}.$$

3.1. Lemma. Suppose that M is a connected matroid having a circuit C such that every connected component of M/C is a circuit. If |E(M)| > 1, then

$$|E(M)| \le |C| + c(M) \left\lceil \frac{c^*(C, M) - 2}{2} \right\rceil.$$
 (*)

Proof. Suppose that (*) is false and choose a counterexample M such that |E(M)| is minimum. Observe that $E(M) \neq C$, otherwise (*) holds. Suppose that M/e is connected for some e in C. Observe that |E(M/e)| > 1 because $E(M) \neq C$. As M/C = (M/e)/(C-e), it follows that the result holds for the pair (M/e, C-e), by the choice of (M, C). In particular,

$$|E(M/e)| \le |C-e| + c(M/e) \left\lceil \frac{c^*(C-e, M/e) - 2}{2} \right\rceil$$

Observe that $c(M/e) \leq c(M)$ and $c^*(C-e, M/e) \leq c^*(C, M)$. Hence

$$|E(M)| - 1 \le |C| - 1 + c(M) \left\lceil \frac{c^*(C, M) - 2}{2} \right\rceil$$

and we obtain the contradiction that (*) holds for M. Thus M/e is disconnected for every e in C.

For all e in C, let P_e be a chord of C such that P_e is the ground set of a connected component of M/e. If possible, choose the chords P_e such that $P_a \neq P_b$ for every 2-element subset $\{a, b\}$ of C. Suppose that this choice is impossible. Then $P_a = P_b$ for some distinct a and b in C. Therefore both $P_a \cup a$ and $P_a \cup b$ are circuits of M. Thus if $x \in P_a$, then M has a circuit contained in $(P_a - x) \cup \{a, b\}$. Suppose that $\{a, b\}$ is a proper subset of C. Then, since $P_a - x$ is independent in M/C, it follows that $(P_a - x) \cup \{a, b\}$ is independent in M; a contradiction. We conclude that $C = \{a, b\}$. Moreover, since, by hypothesis, P_a and P_b cannot be chosen to be different, M is the cycle matroid of the graph consisting of two vertices that are joined by a, b, and a path with edge set P_a . Thus

$$|E(M)| = |C| + |P_a| = 2 + |P_a|,$$

 $c^{*}(C, M) = 3$, and $c(M) = |P_{a}| + 1$. Therefore

$$|C| + c(M) \left[\frac{c^*(C, M) - 2}{2} \right] = 2 + c(M) = 3 + |P_a|$$

and we obtain the contradiction that (*) holds for M.

We now know that if a and b are distinct elements of C, then $P_a \neq P_b$. It follows that $M|[C \cup (\bigcup_{e \in C} P_e)]$ is the cycle matroid of the graph that can be obtained from a cycle with edge set C by adding an edge e' in parallel with each edge e and then subdividing e' to produce a path with edge set P_e . Hence if $C' = \bigcup_{e \in C} P_e$, then C' is a circuit of M, so

$$|C'| \le c(M). \tag{14}$$

Moreover, $M \setminus C'$ is connected. Now, suppose that D^* is a cocircuit of M such that $|D^* \cap C| = 2$, say $D^* \cap C = \{a, b\}$. Observe that D^* must intersect P_a and P_b because $P_a \cup a$ and $P_b \cup b$ are circuits of M. Thus

$$c^*(C, M \setminus C') \le c^*(C, M) - 2.$$
 (15)

By the choice of M, inequality (*) holds for the pair $(M \setminus C', C)$, that is,

$$|E(M \setminus C')| \le |C| + c(M \setminus C') \left\lceil \frac{c^*(C, M \setminus C') - 2}{2} \right\rceil.$$

As $c(M \setminus C') \leq c(M)$, it follows using (15) that

$$|E(M)| - |C'| \le |C| + c(M) \left\lceil \frac{c^*(C, M) - 4}{2} \right\rceil.$$

Hence

$$|E(M)| \leq |C| + c(M) \left[\frac{c^*(C, M) - 2}{2} \right] + |C'| - c(M)$$

$$\leq |C| + c(M) \left[\frac{c^*(C, M) - 2}{2} \right].$$

The last inequality, which holds by (14), gives the contradiction that the lemma holds for M.

3.2. Lemma. Suppose that M is a connected matroid such that |E(M)| > 1. If C is a circuit of M such that |C| = c(M), then

$$|E(M)| \le c(M) \left\lceil \frac{c^*(C,M)}{2} \right\rceil. \tag{**}$$

Proof. Let M_1, M_2, \ldots, M_n be the connected components of M/C. Suppose that (**) is false and choose a counterexample M in which the number of M_i that are not circuits is a minimum. This number is non-zero, otherwise we get a contradiction to Lemma 3.1. In particular, $E(M) \neq C$ and we may assume that M_1 is not a circuit.

Let P be a chord of C that is contained in $E(M_1)$. Fix a circuit C_1 of $M|(C \cup P)$ such that $C_1 - C = P$. Observe that, since P is a series class of $M|(C \cup P)$, this matroid has a circuit D_1 containing $P \cup (C - C_1)$. Then $|C_1| + |D_1| \le 2|C|$. Moreover, $|C_1| = |P| + |C \cap C_1|$ and $|D_1| \ge |P| + |C - C_1|$. Therefore

$$|P| + |C \cap C_1| + |P| + |C - C_1| \le |C_1| + |D_1| \le 2|C|,$$

that is, $2|P| + |C| \le 2|C|$, so

$$2|P| \le |C|. \tag{16}$$

By definition, P is a circuit of the component M_1 of M/C. Thus P is a series class of $(M/C) \setminus (E(M_1) - P)$. Hence

$$P$$
 is a series class of $M \setminus (E(M_1) - P)$. (17)

For each 2-element subset $\{x, y\}$ of C, let N_{xy} be the component of $M/(C - \{x, y\})$ that contains $\{x, y\}$. Choose a 2-element subset $\{a, b\}$ of C such that $E(M_1)$ is contained in $E(N_{ab})$. Then M_1 is a connected component of N_{ab}/a . Thus N_{ab} can be decomposed as the parallel connection of two matroids across a such that,

for one of these matroids, N_1 , we have $N_1/a = M_1$. Among the 2-element subsets $\{a, b\}$ of C for which $E(M_1) \subseteq E(N_{ab})$, choose one such that

$$c_a^*(N_1) - 1 \text{ is a minimum.} \tag{18}$$

Let P_1 be a chord of C contained in $E(M_1)$ such that $|P_1| = c_a(N_1) - 1$. By Theorem 1.5, we have that

$$|E(M_1)| = |E(N_1)| - 1 \le (c_a(N_1) - 1)(c_a^*(N_1) - 1).$$
(19)

Now M_1 is not a circuit. By (17), P_1 is a series class of $M \setminus (E(M_1) - P_1)$. Thus $M \setminus (E(M_1) - P_1)$ is the 2-sum, with basepoint p, of a circuit with ground set $P_1 \cup p$ and a matroid M'_1 with ground set $(E(M) - E(M_1)) \cup p$. We construct a new matroid M' from $M \setminus (E(M_1) - P_1)$ by placing $c_a^*(N_1) - 2$ series classes "in parallel" with P_1 , with each such series class having exactly $|P_1|$ elements. More formally, take $c_a^*(N_1) - 2$ disjoint subsets of size $|P_1|$, each disjoint from E(M), and, for each such subset X, take a circuit with ground set $X \cup p$. Then M' is obtained by deleting p from the parallel connection, with basepoint p, of the collection of matroids consisting of all these circuits, the circuit with ground set $P_1 \cup p$, and the matroid M'_1 . For each 2-element subset $\{x, y\}$ of C, let N'_{xy} be the component of $M'/(C - \{x, y\})$ that contains $\{x, y\}$. Then $E(N'_{ab})$ contains P_1 . Moreover, N'_{ab} can be obtained from N_{ab} by replacing N_1 by the parallel connection, with basepoint a, of $c_a^*(N_1) - 1$ circuits, each having exactly $|P_1| + 1$ elements. Thus

$$|E(M')| = |E(M)| - |E(N_1)| + |P_1|(c_a^*(N_1) - 1) + 1.$$

Since $|P_1| = c_a(N_1) - 1$ and $|E(M_1)| = |E(N_1)| - 1$, we deduce that $|E(M')| - |E(M)| = (c_a(N_1) - 1)(c_a^*(N_1) - 1) - |E(M_1)|.$

Therefore, by (19) , $|E(M')| \ge |E(M)|$. Since M'/C has fewer components that are not circuits than does M/C, it will follow that (**) holds for the pair (M', C), provided we can show that

$$|C| = c(M'). \tag{20}$$

The proof of the lemma will be completed by proving this together with the inequality

$$c^*(C, M') \le c^*(C, M).$$
 (21)

Suppose that c(M') > |C|. Then M' has a circuit C' such that |C'| > |C|. Certainly C' meets E(M') - E(M), otherwise C' is a circuit of M; a contradiction. If $C' \subseteq E(M') - E(M)$, then C' is the union of two series class of M' each of which is contained in E(M') - E(M) and has $|P_1|$ elements. Thus

$$|C'| = 2|P_1| \le |C|$$

where the last inequality follows by (16). This contradiction implies that C' meets both E(M') - E(M) and $E(M) \cap E(M')$. Thus C' contains a series class P' of M'that is contained in E(M') - E(M). Hence $(C' - P') \cup P_1$ is also a circuit of M'having the same cardinality as C'. This is a contradiction because this circuit is also a circuit of M. Thus (20) holds.

Next we prove (21). Suppose that $c^*(C, M') > c^*(C, M)$. Then there is a 2element subset $\{g, h\}$ of C such that

$$c_{qh}^{*}(C, M') > c_{qh}^{*}(C, M),$$

where

$$c_{ab}^*(C, M) = \max\{|D^*| : D^* \in \mathcal{C}(M^*) \text{ and } D^* \cap C = \{g, h\}\}$$

Then certainly $N_{gh} \neq N'_{gh}$. Moreover, if $E(M_1)$ avoids $E(N_{gh})$, then M_1 is a component of $M/(C - \{g, h\})$ and so $N_{gh} = N'_{gh}$; a contradiction. Thus $E(M_1)$ meets, and so is contained in, $E(N_{gh})$. Hence P_1 is contained in $E(N_{gh})$.

Each of N_{gh} and N'_{gh} can be obtained as a parallel connection across g of a certain set of matroids. For N_{gh} , this set S_g includes a matroid N''_1 for which $N''_1/g = M_1$. The set S'_g for N'_{gh} must contain $S_g - \{N''_1\}$.

We show next that $P_1 \cup g$ is a circuit of N''_1 . Suppose not. Then P_1 is a circuit of $M/(C - \{g, h\})$ and hence of $M'/(C - \{g, h\})$. It follows from the construction of M' that P_1 is a component of $M'/(C - \{g, h\})$ and that the elements of E(M') - E(M) are partitioned into $c_a^*(N_1) - 2$ sets each a $|P_1|$ -circuit that is a component of $M'/(C - \{g, h\})$. Thus $S'_g = S_g - \{N''_1\}$ and so $c_{gh}^*(C, M') \leq c_{gh}^*(C, M)$; a contradiction. Thus $P_1 \cup g$ is indeed a circuit of N''_1 .

contradiction. Thus $P_1 \cup g$ is indeed a circuit of N_1'' . The construction of M' now implies that S'_g is obtained from $S_g - \{N_1''\}$ by adjoining $c_a^*(N_1) - 1$ circuits, each of size $|P_1| + 1$. Thus

$$c_{gh}^*(C, M') - c_{gh}^*(C, M) = (c_a^*(N_1) - 1) - (c_g^*(N_1'') - 1).$$

But, by the choice of N_1 , the right-hand side of this equation is not positive. Hence $c_{gh}^*(C, M') \leq c_{gh}^*(C, M)$; a contradiction. We conclude that (21) holds.

Since (**) holds for M' and c(M) = |C| = c(M'),

$$|E(M')| \le c(M) \left\lceil \frac{c^*(C,M')}{2} \right\rceil$$

But $|E(M)| \leq |E(M')|$ and $c^*(C, M') \leq c^*(C, M)$. Hence

$$|E(M)| \le c(M) \left| \frac{c^*(C,M)}{2} \right|$$

a contradiction.

We are now ready to prove the main result of the paper.

Proof of Theorem 1.4. Evidently, it suffices to prove that

$$|E(M)| \le \frac{c(M)c^*(M)}{2}$$

We shall first prove this when $c^*(M)$ is even. By Lemma 3.2,

$$|E(M)| \le c(M) \left\lceil \frac{c^*(C,M)}{2} \right\rceil$$

for any circuit C of M such that |C| = c(M). As $c^*(C, M) \leq c^*(M)$, it follows that

$$|E(M)| \le c(M) \left\lceil \frac{c^*(C,M)}{2} \right\rceil \le c(M) \left\lceil \frac{c^*(M)}{2} \right\rceil = \frac{c(M)c^*(M)}{2},$$

where the last equality follows since $c^*(M)$ is an even integer.

When $c^*(M)$ is odd, let M' be the matroid obtained from M by inserting an element in parallel with each element of the latter. Then $c^*(M') = 2c^*(M)$. Thus $c^*(M')$ is even and hence

$$|E(M')| \le \frac{c(M')c^*(M')}{2}.$$

As $|E(M')| = 2|E(M)|$ and $c(M') = c(M)$, we get
 $2|E(M)| \le c(M)c^*(M)$

and the result follows.

Reid [11] noted that the matroid Ramsey numbers have the property that n(1,1) = 1 and n(s,t) = 2 if s or t is in $\{1,2\}$ provided $(s,t) \neq (1,1)$. As a consequence of the last theorem, we can determine n(s,t) for all other pairs (s,t). Before this result, for $s \leq t$, the Ramsey number with s largest for which the exact value was known was n(6,6), which Bonin, McNulty, and Reid [1] showed was equal to 13.

3.3. Corollary. For all integers s and t exceeding two,

$$n(s,t) = \left\lfloor \frac{(s-1)(t-1)}{2} \right\rfloor + 1.$$

Proof. It follows immediately from Theorem 1.4 that

$$n(s,t) \le \left\lfloor \frac{(s-1)(t-1)}{2} \right\rfloor + 1.$$

We shall show that equality holds here by giving, for all s and t exceeding two, an example of a connected matroid M with c(M) = s - 1, $c^*(M) = t - 1$, and

$$|E(M)| = \left\lfloor \frac{(s-1)(t-1)}{2} \right\rfloor$$

In each case, the matroid M will be the cycle matroid of a graph G. If t is odd, let G be obtained from a cycle with s-1 edges by replacing each edge by $\frac{t-1}{2}$ edges in parallel. If s is odd, let G consist of two vertices joined by t-1 internally disjoint paths each of length $\frac{s-1}{2}$. Finally, if s and t are both even, let G be constructed by joining two vertices by three internally disjoint paths, of lengths $\frac{s-2}{2}$, $\frac{s-2}{2}$, $\frac{s}{2}$, and then replacing each edge of the first and last paths by $\frac{t-2}{2}$ edges in parallel. \Box

4. The matroids attaining the bound of Theorem 1.5

In this section, we characterize the matroids for which equality holds in the bound in Theorem 1.5. In addition, we relate this class of matroids to the graphs that attain the bound in Theorem 1.2. We denote by \mathcal{B}_3 the class of matroids that attain the bound in Theorem 1.5, that is, those matroids M that have an element e for which

$$|E(M)| = c_e(M) + (c_e(M) - 1)(c_e^*(M) - 2).$$

It is straightforward to check that \mathcal{B}_3 is closed under duality and that every circuit with at least two elements including e belongs to \mathcal{B}_3 . In the next lemma, we shall prove that \mathcal{B}_3 is closed under series connection, provided some conditions are satisfied. A matroid M is *uniform with respect to* an element f if every circuit containing f has the same cardinality d and every cocircuit containing f has the same cardinality d^* . A matroid M is a *uniform series connection* of k matroids M_1, M_2, \ldots, M_k for some $k \geq 2$ if the following hold:

- 4.1. (i) M_1, M_2, \ldots, M_k are connected matroids each having at least two elements;
 - (ii) $E(M_i) \cap E(M_j) = \{e\}$ for every 2-subset $\{i, j\}$ of $\{1, 2, ..., k\}$;
 - (iii) $c_e^*(M_1) = c_e^*(M_2) = \dots = c_e^*(M_k);$
 - (iv) M_1, M_2, \ldots, M_k are uniform with respect to e; and
 - (v) M is $S(M_1, M_2, \ldots, M_k)$, the series connection with basepoint e of M_1 , M_2, \ldots, M_k .

4.2. Lemma. Let M_1, M_2, \ldots, M_k be matroids satisfying (4.1)(i)-(ii). Let $M = S(M_1, M_2, \ldots, M_k)$. Then $M \in \mathcal{B}_3$ if and only if all of M_1, M_2, \ldots, M_k are in \mathcal{B}_3 and (4.1)(iii) holds. Moreover, M is uniform with respect to e if and only if (4.1)(iii) and (4.1)(iv) hold.

Proof. Clearly

$$|E(M)| = 1 + \sum_{i=1}^{k} (|E(M_i)| - 1).$$

Thus, by applying Theorem 1.5 to each M_i , we get

$$|E(M)| \leq 1 + \sum_{i=1}^{k} [c_e(M_i) - 1 + (c_e(M_i) - 1)(c_e^*(M_i) - 2)]$$

= $1 + \sum_{i=1}^{k} (c_e(M_i) - 1) + \sum_{i=1}^{k} (c_e(M_i) - 1)(c_e^*(M_i) - 2)$
 $\leq c_e(M) + \max\{c_e^*(M_i) - 2 : 1 \le i \le k\} \sum_{i=1}^{k} (c_e(M_i) - 1)$
= $c_e(M) + \max\{c_e^*(M_i) - 2 : 1 \le i \le k\}(c_e(M) - 1)$
= $c_e(M) + (c_e^*(M) - 2)(c_e(M) - 1).$

Thus $M \in \mathcal{B}_3$ if and only if equality occurs throughout the above, that is, if and only if M_1, M_2, \ldots, M_k are all in \mathcal{B}_3 , and

$$c_e^*(M_1) = c_e^*(M_2) = \dots = c_e^*(M_k).$$

Hence the first part of the lemma holds. The second part follows straightforwardly from the definition of series connection. $\hfill \Box$

The proof of the characterization of the matroids attaining equality in Theorem 1.5 will use two results of Seymour [13]. Because the terminology of that paper is different from that used here, we have translated Seymour's results into our terminology. The first result is [13, (3.7)], restated in the special case that Z is a circuit.

4.3. Lemma. Let e be an element of a circuit C of a connected matroid M. If $M \setminus e$ is connected, then there is no partition $\{X_1, X_2\}$ of E(M) - e such that

- (i) no chord of C meets both X_1 and X_2 ; and
- (ii) for each i in $\{1, 2\}$, if P is a chord of C contained in X_i , then X_i contains a circuit containing P.

The second lemma is a restatement of a special case of [13, (3.8)].

4.4. Lemma. Let C and D be circuits of a matroid M such that $C \cap D = \{e\}$ and $(C \cup D) - e$ is also a circuit of M. Then either $(C \cup D) - e$ has a chord A that is different from $\{e\}$ and is not a chord of C or of D, or there is a partition $\{X_1, X_2\}$ of $E(M \setminus e)$ such that $X_1 \supseteq C - e$ and $X_2 \supseteq D - e$, and

$$r(X_1) + r(X_2) - r(M \setminus e) = r(C - e) + r(D - e) - r((C \cup D) - e) = 1.$$

4.5. Theorem. \mathcal{B}_3 is the minimal class \mathcal{M} of matroids with the following properties.

(i) Every circuit that contains e and has at least two elements is in \mathcal{M} .

- (ii) The dual of every member of \mathcal{M} is in \mathcal{M} .
- (iii) The uniform series connection of a collection of members of \mathcal{M} is in \mathcal{M} .

Proof. We noted above that \mathcal{B}_3 contains all circuits with at least two elements including e and that \mathcal{B}_3 is closed under both duality and uniform series connection. The proof will be completed by proving the following, where the superfluous strength of the assertion will facilitate the argument.

4.5.1. If $M \in \mathcal{B}_3$, then M is uniform with respect to e, and M or its dual is a circuit or is a uniform series connection of a collection of matroids in \mathcal{B}_3 .

We shall argue by induction on |E(M)| noting that the assertion holds when M is a circuit. Thus we may assume that M is not a circuit.

We shall use the same notation as in the proof of Theorem 2.1 taking m = 3. When a matroid attains equality in the bound in Theorem 1.5, equality must hold in all the inequalities that appear in the proof of Theorem 2.1.

First, observe that, to achieve equality in (6), we must have that, for all i in $\{1, 2, \ldots, n\}$,

$$c_e^*(N_i) = c_e^*(N)$$

for every N in Q_i . Thus N_i can be chosen arbitrarily in Q_i .

Equality must also hold in (9), that is,

$$|P_i| = \min\{|L_{ij}| : 1 \le j \le n_i - 1\},\$$

so, for all j in $\{1, 2, \ldots, n_i - 1\}$,

$$|L_{ij}| = |P_i|. \tag{22}$$

Since equality must hold in (10) and (11), we have that, for all i in $\{1, 2, \ldots, n\}$,

$$(n_i - 1)|P_i| = |\mathcal{Q}_i| = |P_i|.$$

Thus, for all such i,

$$n_i = 2. (23)$$

As any N in Q_i can be taken to be N_i , and since equality holds in (13), we have that, for all N in Q_i ,

$$c_e(N) - 1 = \frac{|E(M_i)|}{c_e^*(N) - 1} = \frac{|E(M_i)|}{c_e^*(N_i) - 1} = c_e(N_i) - 1.$$

Hence

$$c_e(N_i) = c_e(N) \tag{24}$$

for every N in Q_i .

Again, since equality holds in (13),

$$|E(M_i)| = |E(N_i)| - 1 = (c_e(N_i) - 1)(c_e^*(N_i) - 1),$$

and so $N_i \in \mathcal{B}_3$. Thus, by the induction assumption, we deduce the following:

4.5.2. For all i in $\{1, 2, ..., n\}$, the matroid N_i is uniform with respect to e.

Next we observe that the chords of C have the following property.

4.5.3. For all *i* in $\{1, 2, ..., n\}$, every chord of *C* contained in $E(M_i)$ has cardinality equal to $|P_i|$.

To see this, we note that, for such a chord P, the set $P \cup e$ is a circuit of N for some N in Q_i . As this N can be taken to be N_i , the assertion follows from (8) and (24) because $|P_i| = c_e(N_i) - 1$ and N_i is uniform with respect to e.

Next we show the following:

4.5.4. Suppose $i \in \{1, 2, ..., n\}$. Let P'_i be a chord of C contained in $E(M_i)$ and let C_i and C'_i be the unique circuits of $M|(C \cup P_i)$ and $M|(C \cup P'_i)$, respectively, that avoid e. Then

$$C_i \cap C = L_{i1} = C'_i \cap C.$$

By definition, $Q_i = \bigcup_{f \in C-e} \{N \in \mathcal{P}_f : N/e = M_i\}$. Also $|\{N \in \mathcal{P}_f : N/e = M_i\}|$ is 0 or 1 for all f in C - e and, by (23), $n_i = 2$. Therefore, from the paragraph between (9) and (10), we have that, for all f in L_{i1} , there is a matroid N in \mathcal{P}_f such that $N/e = M_i$. Thus

$$|\mathcal{Q}_i| = |\{f \in C - e : \mathcal{P}_f \text{ contains a matroid } N \text{ such that } N/e = M_i\}|$$

 $\geq |L_{i1}|.$

But equality holds in (10) and therefore holds in the above. Hence, as $L_{i1} = C_i \cap C$, we have

 $C_i \cap C = \{ f \in C - e : \mathcal{P}_f \text{ contains a matroid } N \text{ such that } N/e = M_i \}.$ (25)

By (4.5.3), $|P'_i| = |P_i|$ and so P'_i can be used in place of P_i throughout the proof of Theorem 2.1 and in the above. In particular,

 $C'_i \cap C = \{f \in C - e : \mathcal{P}_f \text{ contains a matroid } N \text{ such that } N/e = M_i\}.$

We conclude that

$$C_i \cap C = C'_i \cap C,$$

that is, (4.5.4) holds.

On combining (4.5.4) and (22), we get that

$$|C_i \cap C| = |P_i|. \tag{26}$$

We now show that two sets $C_i \cap C$ and $C_j \cap C$ are either disjoint or are comparable.

4.5.5. If i and j are distinct elements of $\{1, 2, \ldots, n\}$, then

(i)
$$C_i \cap C_i = \emptyset$$
; or

(ii) $C_i \cap C \subseteq C_i \cap C$; or

(iii)
$$C_i \cap C \subseteq C_i \cap C$$
.

By (23), $n_i = n_j = 2$. Thus each of the Tutte-lines $C \cup P_i$ and $C \cup P_j$ has size three, so the matroids $M|(C \cup P_i)$ and $M|(C \cup P_j)$ are binary. It is a straightforward consequence of a result of Lemos [8, (3.1)] that $M|(C \cup P_i \cup P_j)$ is also binary. Assume that none of (i)–(iii) holds. Then $M|(C \cup P_i \cup P_j)$ is the cycle matroid of the graph obtained from a cycle C by adding two disjoint paths P_i and P_j each with both endpoints on C such that the resulting graph is a subdivision of K_4 . Let $C' = P_i \cup P_j \cup (C_i \cap C_j \cap C) \cup (C - (C_i \cup C_j))$. Then C' is a circuit of M containing e and

$$|C'| = |P_i| + |P_j| + |C_i \cap C_j \cap C| + |C - (C_i \cup C_j)|.$$

Thus, by (26) and the fact that $\{C_i \cap C_j \cap C, C - (C_i \cup C_j), (C_i \cap C) - C_j, (C_j \cap C) - C_i\}$ is a partition of C, we deduce that

$$|C'| = |C_i \cap C| + |C_j \cap C| + |C| - |(C_i \cap C) - C_j| - |(C_j \cap C) - C_i|$$

As $C_i \cap C$ properly contains $(C_i \cap C) - C_j$, and $C_j \cap C$ properly contains $(C_j \cap C) - C_i$, we deduce that |C'| > |C|; a contradiction. We conclude that (4.5.5) holds.

Let Q_1, Q_2, \ldots, Q_k be the distinct maximal sets in the family $\{C_i \cap C : 1 \leq i \leq n\}$. Then, by (4.5.5), $Q_i \cap Q_j = \emptyset$ for all 2-element subsets $\{i, j\}$ of $\{1, 2, \ldots, k\}$. For each j in $\{1, 2, \ldots, k\}$, let Q'_j be the union of all $E(M_i)$ for which $C_i \cap C \subseteq Q_j$. Then $\{Q'_1, Q'_2, \ldots, Q'_k\}$ is a partition of E(M) - C. Letting $X_1 = Q_1 \cup Q'_1$ and $X_2 = E(M \setminus e) - (Q_1 \cup Q'_1)$, we deduce from Lemma 4.3 that either (a) $M \setminus e$ is disconnected, or (b) X_2 is empty. In the first case, M is a series connection with basepoint e of a set of connected matroids each having at least two elements. Since $M \in \mathcal{B}_3$, it follows by Lemma 4.2 and the induction assumption that M satisfies (4.5.1). Thus we may assume that (ii) holds. Hence

$$k = 1 \text{ and } Q_1 = C - e.$$
 (27)

Moreover, for some i in $\{1, 2, ..., n\}$, we have $Q_1 = C_i \cap C$, and, by (26), $|C_i \cap C| = |P_i|$. Hence $|P_i| = |C| - 1$ and so, letting $D = P_i \cup e$, we have that D is a circuit of M containing e and having cardinality |C|, and $C \cup D$ is a Tutte-line of size three for which the canonical partition is $\{\{e\}, C-e, D-e\}$. Thus $(C \cup D) - e$ is a circuit of M.

Next we shall prove the following:

4.5.6. Let A be a chord of $(C \cup D) - e$. Then $A = \{e\}$, or A is a chord of C or D.

Suppose that $A \neq \{e\}$ and let $W = M | (C \cup D \cup A)$. The proof of (4.5.6) will be broken into several steps the first of which is as follows.

4.5.7. If D_1 and D_2 are circuits of W and $e \in D_1 \cap D_2$, then $D_1 \cup D_2$ is a proper subset of E(W).

Suppose that $E(W) = D_1 \cup D_2$. Then

 $|D_1 \cup D_2| = |E(W)| = |C \cup D \cup A| = |C| + |D - e| + |A| = 2|C| - |\{e\}| + |A| \ge 2|C|.$ As $e \in D_1 \cap D_2$, it follows that $|D_1 \cap D_2| \ge 1$. Thus

$$D_1| + |D_2| = |D_1 \cup D_2| + |D_1 \cap D_2| \ge 2|C| + 1.$$

Therefore $|D_1| > |C|$ or $|D_2| > |C|$. This is a contradiction because $|C| = c_e(M)$ and $e \in D_1 \cap D_2$. We conclude that (4.5.7) holds.

Now consider W^* , which has rank three since W has corank three. Recall that the *points* and *lines* of a matroid are its rank-1 and rank-2 flats.

4.5.8. W^* has the following properties:

- (i) $\{e\}$ and A are points of W^* .
- (ii) If L_1 and L_2 are lines of W^* such that $e \notin L_1 \cup L_2$, then $L_1 \cap L_2 \neq \emptyset$.
- (iii) There are exactly three lines in W^* that contain A.
- (iv) The line which contains e and A does not contain another point.

Part (i) holds because $\{e\}$ and A are series classes of W. Part (ii) follows immediately from (4.5.7) by duality. Parts (iii) and (iv) hold because E(W) - A contains exactly three circuits, and because $E(W) - (A \cup e)$ is the circuit $(C \cup D) - e$ of W. Thus (4.5.8) holds.

By (4.5.8)(iii), W^* has exactly three lines, say L_1, L_2 , and L_3 , that contain A. By (4.5.8)(iv), we may suppose that $L_1 = A \cup e$. Next we establish the following:

4.5.9. L_2 or L_3 contains just one point different from A.

For each i in $\{2, 3\}$, let p_{i1} and p_{i2} be distinct points contained in $L_i - A$. For each j and k in $\{1, 2\}$, let K_{jk} be the line of W^* that contains p_{2j} and p_{3k} . Suppose that, for some i, j, k, and l in $\{1, 2\}$ such that $i \neq k$ and $j \neq l$, we have $e \notin K_{ij} \cup K_{kl}$. Then, by $(4.5.8)(ii), K_{ij} \cap K_{kl} = \{p\}$ for some point p. Clearly p is not on L_2 or L_3 and, by (4.5.8)(iv), it is not on L_1 . Hence p does not exist. Thus K_{ij} or K_{kl} has eas a point. Hence e is on K_{11} or K_{22} , and it is on K_{21} or K_{12} . Thus we have two distinct lines having two common points. This contradiction implies that (4.5.9)holds.

By (4.5.9), we may suppose that $L_2 - A$ contains a single point Z. Then $Z \cup e$ is the complement of L_3 and so is a circuit of W that does not intersect A. Hence $Z \cup e$ is C or D. Moreover, A is a component of $W^* \setminus [(C \cup D) - Z]$ and hence of $W/[(C \cup D) - Z]$. Thus A is a component of $[M|(C \cup D \cup A)]/[(C \cup D) - Z]$. But A is a circuit of $[M|(C \cup D \cup A)]/[C \cup D]$. Hence A is a circuit of $M/[(C \cup D) - Z]$, that is, A is a chord of $(C \cup D) - Z$. Hence A is a chord of C or D, and (4.5.6) holds.

By (4.5.6) and Lemma 4.4, it follows that there is a partition $\{X_1, X_2\}$ of $E(M \setminus e)$ such that $X_1 \supseteq C - e$ and $X_2 \supseteq D - e$, and $r(X_1) + r(X_2) - r(M \setminus e) = 1$. Since e is in the closure of both X_1 and X_2 , it follows that M is the parallel connection, with basepoint e, of matroids with ground sets $X_1 \cup e$ and $X_2 \cup e$. Thus M^* is a series connection, with basepoint e, of two matroids. Since \mathcal{B}_3 is closed under duality, the theorem follows using Lemma 4.2 and the induction assumption.

Evidently each of the matroids that attains the bound in Theorem 1.5 is a series-parallel network. The following result is thus an immediate consequence of Theorems 1.5 and 4.5.

4.6. Corollary. Let M be a connected matroid that is not a series-parallel network. If $e \in E(M)$, then

$$|E(M)| \le (c_e(M) - 1)(c_e^*(M) - 1).$$

We conclude by noting an interesting link between the matroids attaining the bound in Theorem 1.5 and the graphs attaining the bound in Theorem 1.2. The following result was proved by Wu [17].

4.7. **Theorem.** Let M be a connected graphic matroid with at least two elements. Then $E(M) = \frac{1}{2}c(M)c^*(M)$ if and only if, for some $k \ge 2$, there is a sequence of positive integers $(m, n_1, n_2, \ldots, n_k)$ for which $n_i \le \frac{1}{2}\sum_{j=1}^k n_j$ for all i in $\{1, 2, \ldots, k\}$ such that either M or M^* can be obtained by deleting e from the parallel connection, with basepoint e, of k matroids M_1, M_2, \ldots, M_k , where each M_i is the cycle matroid of a graph that can be obtained from a 2-cycle containing e by a sequence of the following operations:

- (a) in a cycle containing e, replacing every edge except e by two edges in parallel; and
- (b) in a bond containing e, subdividing every edge except e exactly once;

where this sequence consists of $n_i - 1$ operations of type (a) and m - 1 operations of type (b).

Using Theorem 4.5, one can show that the matroids in \mathcal{B}_3 can be characterized in a similar way. To prevent the paper from becoming too long, we omit the details of the proof. 4.8. Corollary. Let M be a connected matroid with at least two elements and suppose $e \in E(M)$. Then $|E(M)| = (c_e(M) - 1)(c_e^*(M) - 1) + 1$ if and only if, for some $k \ge 1$, there is a sequence of positive integers $(m, n_1, n_2, \ldots, n_k)$ such that M is the parallel connection, with basepoint e, of k matroids M_1, M_2, \ldots, M_k , where each M_i is the cycle matroid of a graph that can be obtained from a 2-cycle containing e by a sequence of the following operations:

- (a) in a cycle containing e, replacing every edge except e by two edges in parallel; and
- (b) in a bond containing e, subdividing every edge except e exactly once;

where this sequence consists of $n_i - 1$ operations of type (a) and m - 1 operations of type (b).

Acknowledgements

The authors thank Guoli Ding for showing them a short proof of Theorem 1.5. The first author was partially supported by CNPq, CAPES, FINEP, and PRONEX 107/97. The second author was partially supported by the National Security Agency.

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